

ABSTRACT OF THE DISCLOSURE

A clock generating device for accurately generating a reference clock signal synchronized with a wobble signal
5 including address information recorded by phase modulating a groove wobble. The clock generating device includes a detection circuit, which monitors the wobble signal including the address information recorded through phase modulation. The detection circuit also detects the period in
10 which the phase-modulated section of the wobble data signal is provided to a PLL circuit. When the period is detected, the detection circuit generates a hold signal to hold the output of the PLL circuit.